UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):	Michael H. Perrott
Assignee:	Silicon Laboratories Inc.
Title:	METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION

Application No.:	09/888,708	Filed:	June 25, 2001
Examiner:	Guy J. Lamarre	Group Art Unit:	2133
Atty. Docket No.:	026-0015	Conf. No.:	8814

COMMISSIONER FOR PATENTS
P.O. Box 1450
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POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(INCLUDING REVOCATION OF PRIOR POWERS)
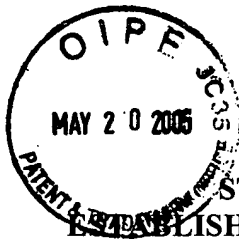
Dear Sir:

Assignee of record of the entire interest of the above-identified application hereby revokes all powers of attorney previously given and appoints the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Mark Zagorin (36,067); Andrew C. Graham (36,531); David W. O'Brien (40,107); Nicole Teitler Cave (54,021); Steven R. Gilliam (51,734); and Nestor Ho (39,460).

CORRESPONDENCE ADDRESS

Please direct all correspondence concerning this application according to the USPTO Customer Number, if provided below, or otherwise to the individual and/or firm named below:

Customer Number
22120
(Zagorin O'Brien Graham LLP)

**STATEMENT UNDER 37 CFR § 3.73(b)
ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION**

The assignee, Silicon Laboratories Inc., seeks to take action in the above-identified patent application. Chain of title from the inventors of the above-identified patent application to the assignee is as follows:

- An assignment from Michael H. Perrott to Silicon Laboratories Inc. is recorded in the Patent and Trademark Office at Reel 011935/Frame 0844.

The undersigned, whose title is given below, is authorized to act on behalf of the assignee.

Date 5/11/05

Brad J. Fluke
Vice President, General Manager
Networking Products
Silicon Laboratories Inc.

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